



SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

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61352-043SERIAL NO.
10/657,799APPLICANT
Takeshi TAKAGIFILING DATE
September 09, 2003GROUP
2811

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
PC		US 6,040,810	03/21/2000	Noguchi et al.	
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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
PC		JP P2001-210831A	08/03/2001	MATSUSHITA ELECTRIC IND CO LTD		(Japan w/English Abstract)	
PC		EP 1 102 327 A2	05/23/2001	MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD		X	
PC		EP 0 921 575 A2	06/09/1999	MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.		X	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
PC		TAKAGI A. et al., "A Novel High Performance SiGe Channel Heterostructure Dynamic Threshold pMOSFET (HDTMOS)", IEEE ELECTRON DEVICE LETTERS, 2001, May, Vol.22, No.5, pages 206-208, full text
PC		TAKAMIYA M. et al., "High Drive-Current Electrically Induced Body Dynamic Threshold SOI MOSFET (EIB-DTMOS) with Large Body Effect and Low Threshold Voltage", IEEE TRANSACTIONS ON ELECTRON DEVICES, 2001, August, Vol.48, No.8, pages 1833 to 1840, full text

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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						Yes	No
PC		JP P2000-260991 A	09/22/2000	UNIV TOKYO		Japanese (w/ English Abstract)	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
PC		Toshiro HIRAMOTO et al., "LOW POWER AND LOW VOLTAGE MOSFETS WITH VARIABLE THRESHOLD VOLTAGE CONTROLLED BY BACK-BIAS", IEICE TRANS. ELECTRON., VOL. E83-C, NO. 2 February 2000, pp. 181-189
PC		Tadahiro KURODA et al., "A 0.9-V, 150-MHz, 10-mW, 4 mm ² , 2-D DISCRETE COSINE TRANSFORM CORE PROCESSOR WITH VARIABLE THRESHOLD-VOLTAGE (VT) SCHEME", IEEE Journal of Solid-State Circuits, Vol., 31, No. 11, November 1996, pp. 1770-1779

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